

E1/ T1/ Datacom BERT HCT-BERT/H



E1/ T1/ Datacom Bit Error Rate tester

The HCT-BERT/H Bit Error Rate tester is a compact, notebook sized E1/T1 PCM measuring instrument designed for field use in analysis and maintenance of E1 (2.048Mbps) or T1 (1.544Mbps) lines. The HCT/BERT-H performs framed, unframed, signaling analysis, drop and insert Nx64Kbps, or nx56Kbps data into any time slot. The HCT-BERT/H series analyzer also provides a variety of E1 or T1 line statuses, transmission performance testing (BERT) and monitoring. On the E1 or T1 line, the HCT-BERT/H series product may be used as a generator or receiver.

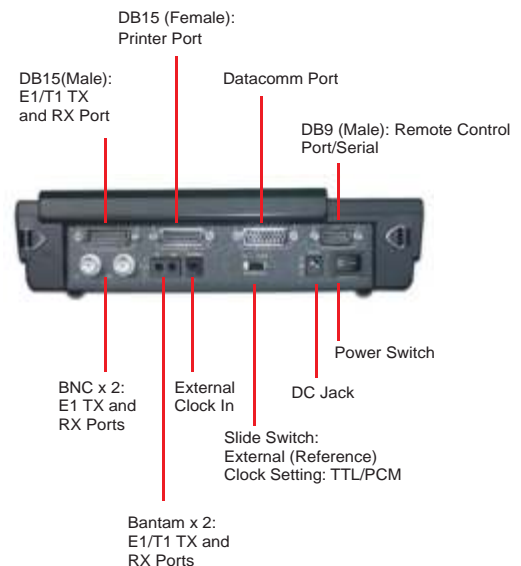
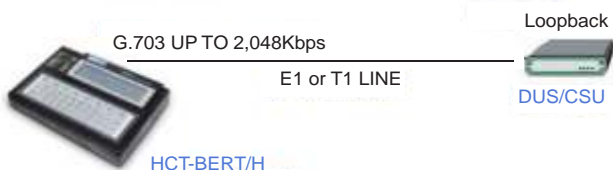
Features

- Internal Memory storage of test result. / Direct display on LCD screen
- Loop Back Code Setting and Detection: IN Band, Out Band and ITU-T V.54 BERT Histogram Analysis
- Portable for field use
- Results Report
- Round Trip Delay Measurement
- Signaling Display: Display all channels of ABCD bits
- Supports CRC, and BPV performance analysis and generator
- User Programmable Pattern Setting: There are three 32 bit programmable patterns, which can be inserted onto the E1/T1 line and drop for analysis
- Upgradeable for advanced features
- DS0 Control Loop Codes (optional): TIP, LSC, LBE, FEV
- Print out via Parallel Printer port / Print out via RS-232 Series Port (option)

Specifications

Standard	ITU Q.921,Q.931	
Timeslot	Setting	Available, bypassed, or idle timeslot, Drop and Insert N x 64k data onto E1/T1 line
	Mapping Data	Analyze any channel data of two frames
LCD display	32 Characters x 8 Lines, Text/	
Power	AC	100 — 240VAC Adapter
	DC	12VDC
Environment	Temperature	0 — 50°C (Operating); -20 — 65°C (Storage)
	Humidity	0 — 90% non condensing
Power Consumption	TBA	
Dimensions(WxDxH)	220mm x 65mm x 275mm	
Weight	1.6Kg	

Application



Ordering Info

HCT-BERT/H E1/T1 Datacom BERT Analyzer